



NVIDIA CONNECTX-7 NDR 400G INFINIBAND ADAPTER CARD

ACCELERATE DATA-DRIVEN SCIENTIFIC COMPUTING WITH IN-NETWORK COMPUTING

The NVIDIA® ConnectX®-7 NDR 400 gigabits per second (Gb/s) InfiniBand host channel adapter (HCA) provides the highest networking performance available to take on the world's most challenging workloads. The ConnectX-7 InfiniBand adapter provides ultra-low latency, 400Gb/s throughput, and innovative NVIDIA In-Network Computing engines to deliver the acceleration, scalability, and feature-rich technology needed for high performance computing (HPC), artificial intelligence (AI), and hyperscale cloud data centers.

High performance computing and artificial intelligence have driven supercomputers into wide commercial use as the primary data processing engines enabling research, scientific discoveries and product development. These systems can carry complex simulations and unlock the new era of AI, where software writes software. NVIDIA InfiniBand networking is the engine of these platforms delivering breakthrough performance.

ConnectX-7 NDR InfiniBand smart In-Network Computing acceleration engines include collective accelerations, MPI Tag Matching and All-to-All engines, and programmable datapath accelerators. These performance advantages and the standard guarantee of backward- and forward-compatibility ensure leading performance and scalability for compute and data-intensive applications and enable users to protect their data center investments.

PORTFOLIO

- > Single-port or dual-port NDR (400Gb/s) or NDR200 (200Gb/s), with octal small form-factor pluggable (0SFP) connectors
- > Dual-port HDR (200Gb/s) with quad small form-factor pluggable (QSFP) connectors
- > PCIe standup half-height, half-length (HHHL) and full-height, half-length (FHHL) form factors, with options for NVIDIA Socket Direct[™]
- > Open Compute Project 3.0 (OCP3.0) tall small form factor (TSFF) and small form factor (SFF)
- > Standalone ConnectX-7 application-specific integrated circuit (ASIC), supporting PCIe switch capabilities

PRODUCT SPECIFICATIONS

Max total bandwidth	400Gb/s
IBTA Spec compliant	1.5
Number of network ports	1/2/4
Host interface	PCIe Gen5, up to x32 lanes
RDMA message rate	330-370 million messages per second
Acceleration	Collective operations
engines	MPI All-to-All
	MPI tag matching
	Programmable datapath accelerator
Advanced storage capabilities	Block level encryption and checksum offloads
Accurate timing	PTP 1558v2, 16ns accuracy
Secure boot	On-chip with hardware root-of-trust
Host management	NC-SI, MCTP over SMBus, and MCTP over PCIe
Supported operating systems	Linux, Windows, VMware
Form factors	PCIe HHHL, FHHL, Socket Direct OCP3.0 TSFF, SFF

FEATURES*

InfiniBand

- InfiniBand Trade Association (IBTA)
 Specification 1.5 compliant
- > Up to four ports
- Remote direct-memory access (RDMA), send/receive semantics
- > Hardware-based congestion control
- > Atomic operations
- > 16 million input/output (IO) channels
- > 256 to 4Kbyte maximum transmission unit (MTU), 2Gbyte messages
- > 8x virtual lanes (VL) + VL15

Enhanced Networking

- > Hardware-based reliable transport
- > Extended Reliable Connected (XRC) transport
- > Dynamically Connected Transport (DCT)
- > GPUDirect[®] RDMA
- > GPUDirect Storage
- > Out-of-order RDMA supporting adaptive routing
- > Enhanced atomic operations
- > Advanced memory mapping support, allowing user mode registration and remapping of memory (UMR)
- On-demand paging (ODP), including registration-free RDMA memory access
- > Enhanced congestion control
- > Burst buffer offload

In-Network Computing

- > Collective operations offloads
- > Vector collective operations offloads
- > MPI tag matching
- > MPI All-to-All offloads
- > Rendezvous protocol offload
- > In-network memory
- > Programmable datapath accelerator

Hardware-Based IO Virtualization

> Single root IO virtualization (SR-IOV)

Storage Offloads

> Block-level encryption:

XTS-AES 256/512-bit key

- > NVMe over Fabrics (NVMe-oF) offloads for target machine
- > T10 Data Integrity Field (DIF) signature handover operation at wire speed for ingress and egress traffic
- > Storage protocols: SRP, iSER, NFS RDMA, SMB Direct, NVMe-oF

HPC Software Libraries

> NVIDIA HPC-X[®] and UCX[®], UCC, NCCL,
 OpenMPI, MVAPICH, MPICH, OpenSHMEM,
 PGAS, and various commercial packages

Management and Control

- > NC-SI, MCTP over SMBus, and MCTP over PCIe
- > PLDM for Monitor and Control DSP0248
- > PLDM for Firmware Update DSP0267
- > PLDM for Redfish Device Enablement DSP0218
- > PLDM for FRU DSP0257
- > SPDM DSP0274
- > General-purpose IO pins
- > Serial Peripheral Interface (SPI) to flash
- > JTAG IEEE 1149.1 and IEEE 1149.6

Remote Boot

- > Remote boot over InfiniBand
- > Remote boot over Internet Small Computer Systems Interface (iSCSI)
- > Unified Extensible Firmware Interface (UEFI)
- > Preboot Execution Environment (PXE)

Security

- > Secure boot with hardware root of trust
- > Secure firmware update
- > Flash encryption

Advanced Timing and Synchronization

- > Advanced PTP
- > IEEE 1588v2 (any profile)
- > Line-rate hardware timestamp (UTC format)
- > Configurable PPS In and configurable
 - PPS Out
 - > Time-triggered scheduling
- > PTP-based packet pacing
- > Time-Sensitive Networking (TSN)

COMPATIBILITY

PCI Express Interface

- > PCIe Gen 5.0 compatible, 32 lanes
- > Support for PCIe x1, x2, x4, x8, and x16 configurations
- > NVIDIA Multi-Host[™] supports connection of up to 8x hosts
- > PCIe Atomic
- Transaction layer packet (TLP) processing hints (TPH)
- PCIe switch Downstream Port Containment (DPC)
- > Advanced error reporting (AER)
- > Access Control Service (ACS) for peer-topeer secure communication
- > Process Address Space ID (PASID)
- > Address translation services (ATS)
- > Support for MSI/MSI-X mechanisms
- > Support for SR-IOV

Operating Systems/Distributions*

- > In-box drivers for major operating systems:
 - > Linux: RHEL, Ubuntu
 - > Windows
- > Virtualization and containers
 - > VMware ESXi (SR-IOV)
 - Kubernetes
- > OpenFabrics Enterprise Distribution (OFED)
- > OpenFabrics Windows Distribution (WinOF-2)

ADAPTER CARD PORTFOLIO AND ORDERING INFORMATION

PCIE STANDUP FORM FACTOR

InfiniBand Supported Speeds [Gb/s]	Network Ports and Cages	Host Interface [PCle]	Form Factor / Feature Support	Orderable Part Number (OPN)
NDR/NDR200	1x 0SFP	PCIe Gen 4.0/5.0 x16	HHHL	MCX75510AAN-NEAT
	1x 0SFP	With option for extension	HHHL	MCX75310AAN-NEAT
	1x OSFP	PCle Gen 4.0/5.0 x16	HHHL	MCX75510AAS-NEAT
	2x 0SFP	PCle Gen 4.0/5.0 x16	Secure boot	MCX75511BAN-NEAT
NDR200	1x 0SFP	PCIe Gen 4.0/5.0 x16	HHHL	MCX75510AAN-HEAT
	1x 0SFP	With option for extension	HHHL	MCX75310AAN-HEAT
	2x 0SFP	PCle Gen 4.0/5.0 x16	FHHL	MCX75511BAN-HEAT
HDR/HDR100/EDR	1x QSFP	PCIe Gen 4.0/5.0 x16	HHHL	MCX755105AN-HEAT
	2x QSFP	With option for extension	HHHL	MCX755106AN-HEAT

Dimensions without brackets are 167.65mm x 68.90mm. All adapters are shipped with the tall bracket

mounted and a short bracket as an accessory

OCP 3.0 SMALL FORM FACTOR

InfiniBand Supported Speeds [Gb/s]	Network Ports and Cages	Host Interface [PCIe]	OPN
NDR/NDR200/	1x OSFP	PCIe Gen 4.0/5.0 x16 TSFF	MCX75343AAN-NEAB ¹
HDR/HDR100	2x QSFP56	PCIe Gen 4.0/5.0 x16 SFF	MCX753436AN-HEAB

The last digit of the OPN-suffix displays the default bracket option: B = pull tab, I = internal lock; E = ejector latch. For other bracket types, contact NVIDIA. Note 1.Pre OCP3.2 Spec

IC ORDERING INFORMATION

Product Description	OPN
ConnectX-7 2-port IC, NDR, PCIe 5.0 x32, No Crypto	MT29108A0-NCCF-NV
ConnectX-7 2-port IC, NDR, Multi-Host, PCIe 5.0 x32, No Crypto	MT29108A0-NCCF-NVM
ConnectX-7 2-port IC, NDR, Multi-Host, PCIe 5.0 x32, Crypto	MT29108A0-CCCF-NVM

Learn more at NVIDIA ConnectX-7 InfiniBand

© 2021 NVIDIA Corporation. All rights reserved. NVIDIA, the NVIDIA logo, ConnectX, GPUDirect, HPC-X, Mellanox, Multi-Host, PeerDirect, Scalable Hierarchical Aggregation and Reduction Protocol (SHARP), and UCX are trademarks and/ or registered trademarks of NVIDIA Corporation in the U.S. and other countries. Other company and product names may be trademarks of the respective companies with which they are associated. All other trademarks are property of their respective owners. APR21

